



Docket No.: M4065.0400/P400-A  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

Jeffery W. Janzen et al.

Allowed: October 6, 2004

Application No.: 10/720,183

Confirmation No.: 4215

Filed: November 25, 2003

Art Unit: 2187

For: SYSTEM LATENCY LEVELIZATION  
FOR READ DATA

Examiner: Ngoc V. Dinh

**COMMENTS ON EXAMINER'S  
STATEMENT OF REASONS FOR ALLOWANCE**

U.S. Patent and Trademark Office  
220 20th Street S.  
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Crystal Plaza Two, Lobby, Room 1B03  
Arlington, VA 22202

Dear Sir:

Applicants agree in part with the Examiner's statement for allowance in that the prior art of record fails to disclose the limitations noted by the Examiner. However, the Examiner's statement does not reflect all of the language of each of the allowed claims. The totality of the language of each claim provides additional reasons for the allowance of each claim.

Dated: December 7, 2004

Respectfully submitted,

By \_\_\_\_\_

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